

# PhD Entrance Examination Date: 18-12-2018Time:11am to 2pmVenue : Room No. 14, Department of Electronics, Kuvempu University

### **Question Paper Pattern & Exam Details**

- 1. Maximum marks for entrance test 90
- **2.** Nature of questions: Objective type-multiple choice questions, short notes and essay type questions.
- **3.** No. of questions: 20 question each carrying 1 marks. For descriptive questions, eight short note and six essay type questions, out of which any five short notes and four essay type questions need to be answered. Each short note and essay type question carry 6 marks and 10 marks respectively. 50% of questions shall be from Research Methodology and the remaining from the Cognate subjects of Electronics.
- 4. Viva voce: 10 marks for those who have qualify entrance exam.
- **5.** Minimum passing marks: 50% of total marks for which entrance is conducted with relaxation of 5% to SC/ST/OBC(non-creamy layer)/differently abled category candidates (i.e., Minimum is 45% i.e., 40 marks). There shall be no minimum marks for pass in the entrance test for those candidates who have qualified UGC/CSIR NET (including Lectureship/SLET/teacher fellowship holder/Inspire fellowships).

#### Syllabus for Entrance Exam:

## **RESEARCH METHODOLOGY**

- Types of research & Research process
- Data collection and presentation
- Basic statistical measures
- Probability distributions
- Sampling & Distributions

# COGNATE SUBJECTS

- Analog & Digital Electronics
- Programming in C with Data Structures
- Computer Organization
- Signals and Systems
- EM Theory, Microwave Devices and Radar
- Microprocessors and Microcontrollers
- Digital Signal and Image Processing
- Control System
- Electronic Instrumentation and Measurements
- Advanced Communication System
- Digital design using VHDL
- Embedded System Design
- VLSI Design
- Computer Communication Networks
- Information Theory and Coding